

10/070879#3

JC10

PCT/PTO 04 MAR 2002

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE**INFORMATION DISCLOSURE
STATEMENT**Docket Number:
10191/2261Application Number
To Be AssignedFiling Date
Herewith

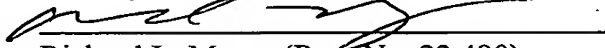
Examiner

Art Unit

Invention Title
SYSTEM FOR PROTECTING A POWER
SEMICONDUCTOR OUTPUT STAGE THAT
SWITCHES AN INDUCTIVE LOADInventor(s)
Martin KESSLER and Stefan KOCHAddress to:
Assistant Commissioner for Patents
Washington D.C. 20231
Box Patent Application

- I. In accordance with the duty of disclosure under 37 C.F.R. § 1.56 and in conformance with the procedures of 37 C.F.R. §§ 1.97 and 1.98 and M.P.E.P. § 609, attorneys for Applicants hereby bring the following references to the attention of the Examiner. The references are listed on the attached modified PTO Form No. 1449. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that these references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.
- II. A copy of each patent, publication or other information listed on the modified PTO form 1449 is enclosed, except as otherwise indicated on the modified PTO form 1449.

Dated: 3/4/02

By: 
Richard L. Mayer (Reg. No. 22,490)KENYON & KENYON
One Broadway
New York, N.Y. 10004
(212) 425-7200 (telephone)
(212) 425-5288 (facsimile)

EL 828171024US